Notice of References Cited Application/Control No. 10/785,074 Examiner William M. Brewster Applicant(s)/Patent Under Reexamination SEKINE ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,649,538 B1	11-2003	Cheng et al.	438/775
	В	US-			
	С	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-	\		
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

	TORLIGH FAILHT BOODINERTS						
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification	
	N						
	0						
	Р						
	σ						
	R						
	S						
	Т						

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	Stanley Wolf Ph.D. and Richard N. Tauber Ph.D. in Silicon Processing for the VLSI Era, Volume 1: Process Technology, Lattice Press, 1986, pp. 242-61.		
	V			
	w			
	х			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.